

**INDIAN INSTITUTE OF TECHNOLOGY, ROORKEE  
(ECE Department)**

Dated: February 06, 2024

**ADVERTISEMENT TO FILL UP PROJECT POSITIONS\***

Applications are invited from Indian nationals only for project position(s) as per the details given below for the consultancy/research project(s) under the Principal investigator (Name: **Prof. Sudeb Dasgupta**), Dept./Centre: **Electronics and Communication Engineering**, Indian Institute of Technology, Roorkee.

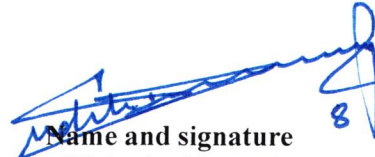
1. Title of project: A novel power-on pilot IC for ultra-low power wireless IoT devices
  2. Sponsor of the project: Ministry of Electronics and Information Technology (MeitY) under C2S programme **(Tier-I)**
  3. Project position(s) and number: Project Fellow (01) under the supervision of **(Co-PI: Prof. Bishnu Prasad Das)**
  4. Qualifications: B.E/B.Tech in ECE or equivalent with 6 years of experience or M.E/M.Tech in VLSI or equivalent with 3 years of experience
  5. Emoluments: Rs. 45,000 + HRA (as per institute rules)
  6. Duration: Initially. for ONE- year (extendable up to two years based on performance)
  7. Job description: The candidate will be responsible for the design and implementation of digital hardware circuits for various applications. The candidate should know the following:
    - Digital integrated circuits, Hardware description languages: Verilog or System Verilog (Desirable)
    - RTL simulation: Xilinx Vivado or Modelsim or VCS (Desirable)
    - Scripting languages: Python or Perl (Preferred)
    - Physical Synthesis tools: Synopsys IC-compilier or Cadence Innovus (Preferred)
    - Knowledge about Static timing analysis (STA) tool (Preferred)
- a) Candidates before appearing for the interview shall ensure that they are eligible for the position they intend to apply.
  - b) Candidates desiring to appear for the Interview should submit their applications with the following documents to the office of Principal Investigator through email ([sudeb.dasgupta@ece.iitr.ac.in](mailto:sudeb.dasgupta@ece.iitr.ac.in)) keeping ([vlsi-mtech@ece.iitr.ac.in](mailto:vlsi-mtech@ece.iitr.ac.in)) in CC
    - Application in a plain paper with detailed CV including chronological discipline of degree/certificates obtained.
    - Experience including research, industrial field and others.
    - Attested copies of degree/certificate and experience certificate.
    - **Subject of the mail should be “Application for the post of Project Fellow in a project entitled (project name)”**
  - c) Candidate shall bring along with them the original degree(s)/certificate(s) and experience certificate(s) at the time of interview for verification.
  - d) Preference will be given to SC/ST candidates on equal qualifications and experience.
  - e) Please note that no TA/DA is admissible for attending the interview.
- Note: The selected candidate may get an opportunity for PhD admission.(as per institute rule, if applicable).**

Walkin-interview date and time: February 22, 2024 at 12:00 PM.

Meeting Link: <https://iitroorkee.webex.com/iitroorkee/j.php?MTID=m2b768f30552869b488665a05857a5c6f>

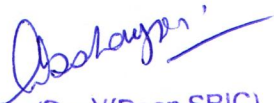
Tel: +91-1332-286415

Email: [vlsi-mtech@ece.iitr.ac.in](mailto:vlsi-mtech@ece.iitr.ac.in)

  
8/2/2024  
Name and signature  
of Principal Investigator

\*To be uploaded on IIT Roorkee website and copy may be sent to appropriate addresses by PI for wider circulation.

Approved



डॉन (सिक)/(Dean SRIC)  
भा. प्रौ. सं. रुड़की/IIT Roorkee

Rajeev  
13-2-2024 14/2/24