SRIC/06

INDIAN INSTITUTE OF TECHNOLOGY, ROORKEE
(Department of Electronics and Communication Engineering)

Dated: 3rd May 2026

ADVERTISEMENT TO FILL UP PROJECT POSITIONS*

Applications are invited from Indian nationals only for Project Associate position(s) as per the details given below for the consultancy project(s) under the Principal investigator (Name: **Prof. Bishnu Prasad Das**), Department of **Electronics and Communication Engineering, Indian Institute of Technology, Roorkee.**

1. Title of project : **Adaptive digital design for Variation resilient ultra-low voltage and low power operation**
2. Sponsor of the project: STMicroelectronics, Noida.
3. Project position(s) and number: Project Associate (Two positions)
4. Qualifications: B.Tech. degree in Electronics and Communication Engineering or equivalent with **Qualified GATE score**.
5. Emoluments: Rs 60,000/- per month (which includes HRA)
6. Duration: for ONE- year (extendable up to three years based on performance)
7. **Job description:** The candidate will be responsible for the design and implementation of adaptive systems in chip-level. The knowledge of Digital VLSI Circuit Design, static timing analysis, processor architecture are essential. The candidate should have experience in RTL design, verification, simulation and Physical Design. The candidate should have experience in Cadence Virtuoso, SPICE simulations including parasitic extracted netlist simulation and Monte-Carlo simulation. The candidate should have a good knowledge of semiconductor device physics, network theory, and CMOS VLSI circuit design. **The candidate may get an opportunity for PhD admission.**
8. The candidate should have enthusiasm to coordinate and work at pace with collaborating industry.
9. Candidates before appearing for the interview shall ensure that they are eligible for the position they intend to apply.
10. Candidates desiring to appear for the Interview should submit their applications with the following documents to the office of Principal Investigator through email, (bishnu.das@ece.iitr.ac.in)
    - Application in a plain paper with detailed CV including chronological discipline of degree/certificates obtained.
    - Experience including research, industrial field and others.
    - Attested copies of degree/certificate and experience certificate. **Subject of the email should be "Application for the post of Project Associate in a project entitled "Adaptive digital design for Variation resilient ultra-low voltage and low power operation"**
11. Candidate shall bring along with them the original degree(s)/certificate(s) and experience certificate(s) at the time of interview for verification.
12. Preference will be given to SC/ST candidates on equal qualifications and experience.
13. Please note that no TA/DA is admissible for attending the interview.

The last date for application to be submitted to the Principal Investigator through email is May 13, 2026 (Wednesday) by 5 PM. The interview will be held in online mode on May 14, 2026 (Thursday) at 10:00 AM.

Tel: +91-1332-284798
Email: bishnu.das@ece.iitr.ac.in

Name and signature of Principal Investigator
(Prof. Bishnu Prasad Das)

*To be uploaded on IIT Roorkee website and copy may be sent to appropriate addresses by PI for wider circulation.

Dean
Sponsored Research & Industrial Consultancy
Indian Institute of Technology Roorkee
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