

INDIAN INSTITUTE OF TECHNOLOGY, ROORKEE
Electronics & Communication Engineering Department

Dated:

ADVERTISEMENT TO FILL UP PROJECT POSITIONS*

Applications are invited from Indian nationals only for project position(s) as per the details given below for the consultancy/research project(s) under the principal investigator (**Name: Prof. Anand Bulusu**), Dept./Centre **ECE Dept.** Indian Institute of Technology, Roorkee.

1. Title of project: A Parasitic and mechanical stress aware design methodology for novel EDA compatible buffer-TSV standard cells for 3D ICs.
2. Sponsor of the project: ANRF Delhi
3. Project position(s) and number: JRF One (1)
4. Qualifications: B.E/B.Tech in ECE or equivalent (Preferably, M.E/M.Tech Microelectronics/VLSI or equivalent) with Gate qualified.
5. Emoluments: Rs. 37000/- + HRA (as per institute rules)
6. Duration: One Year
 - Extendible till the end of the project based on periodic review after six months.
7. Job description: The candidate should have a good knowledge of semiconductor device physics, network theory and VLSI circuit design. He/She should have an exposure to CMOS design logic.
 - EDA/TCAD Tools: Cadence tools suite, SPICE, Sentaurus TCAD device/process simulation.
 - Hardware Descriptive language: Verilog
 - MATLAB, Python
8. The candidate should have enthusiasm to coordinate and work at pace with collaborating industry
9. Candidates before appearing for the interview shall ensure that they are eligible for the position they intend to apply for.
10. Candidates desiring to appear for the interview should submit their applications with the following documents to the office of Principal Investigator through by offline/Physical mode
 - **Subject of the mail should be "Application for the post of JRF in ANR-3032-ECD-TSAH/26-27"**
11. Candidate shall bring along with them the original degree(s)/certificate(s) and experience certificate(s) at the time of interview for verification.
12. Preference will be given to SC/ST candidates on equal qualifications and experience.
13. Please note that no TA/DA is admissible for attending the interview
14. Note: The selected candidate may get an opportunity for PhD admission. GATE mandatory
15. The last date for application to be submitted to office of Principal investigator is JUNE 30, 2026
16. The date of online interview shall be informed to the shortlisted candidates by email.

B. Anand

APPROVED

Name and signature
of Principal Investigator

Email: vlsi-office@ece.iitr.ac.in

*To be uploaded on IIT Roorkee website and copy may be sent to appropriate addresses by PI for wider circulation.

Dean
 Sponsored Research & Industrial Consultancy
 Indian Institute of Technology Roorkee
 Roorkee-247 667 (INDIA)

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