



**INDIAN INSTITUTE OF TECHNOLOGY ROORKEE**  
(Electronics and Communications Engineering)

Dated: 08/05/2026

**ADVERTISEMENT TO FILL UP PROJECT POSITIONS\***

Applications are invited from Indian nationals only for project position(s) as per the details given below for the consultancy/research project(s) under the Principal investigator (Name: **Dr. Abhay Kumar Sah**), Dept. **Electronics and Communications Engineering**, Indian Institute of Technology Roorkee.

1. **Title of project:** RIS-Enabled Hybrid RF/VLC System for improving QoE
2. **Sponsor of the project:** C-DoT, Dept of Telecommunication (DoT)
3. **Project position(s) and number:** Junior Research Fellow (JRF): 01 (one position)
4. **Qualifications:**
  - Master's or Bachelor's degree in ECE/EE/CSE or equivalent with CGPA above 7.0 (on 10.0 scale) or percentage greater than 70% aggregate irrespective of category.
  - It is desirable for the candidate to have qualified GATE or National Eligibility Tests- CSIR-UGC NET including lectureship. (**Note:** Bachelor's or master's from IITs, NITs, IIITs, and any other CFTIs with a CGPA 8.0 and above on 10 scale point are exempted from the requirement of GATE/NET.)
5. **Emoluments:** 37,000/- per month and 9% HRA (as applicable).
6. **Duration:** 1 year, (can be extended up to project duration based on performance reviews).
7. **Job description:** The JRF will be involved in the research and development activities related to cell-free massive MIMO systems for 5G/6G networks. It consists of developing baseband algorithms utilizing signal processing and deep learning techniques, and subsequently, prototyping them on an RFSoc/USRP board. It will also involve documenting and presenting the research outputs to various forums. The candidate is expected to have
  - Sound knowledge of Linear algebra, Probability theory, Random processes, and Wireless communication.
  - Strong programming skills in C/C++/Python/MATLAB etc.
  - Experience in handling RFSoc/MPSoc/FPGA/USRP boards.
  - Experience in writing papers/patents/technical report etc.

**Application Process**

1. Candidates before applying for the position shall ensure that they are eligible for the position they intend to apply.
2. Candidates desiring to appear for the Interview should fill the google form application either using the URL <https://forms.gle/H3ywTFx3e3UkMvc67> or scanning the QR code at the top.
3. Candidate should also email the following documents to the program manager (6g-vigyan@ece.iitr.ac.in) with subject as "Application for Project Position" and the file names as FirstName\_LastName\_DocName:
  - Latest CV including chronological discipline of degree/certificates obtained and work experience (if any).
  - Scanned copies of all the degree and experience certificates in a single PDF files.
4. The interview will be online and the candidate may be asked to show the original degree(s)/certificate(s) and experience certificate(s) at the time of interview/joining for verification.
5. Preference will be given to SC/ST candidates on equal qualifications and experience.

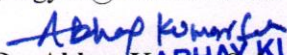
**Note: The selected candidate may get an opportunity for PhD admission subject to performance and satisfying the qualification criteria.**

**It will be a rolling advertisement until the positions are filled.**

The first deadline for application to be submitted to the email ID: 6g-vigyan@ece.iitr.ac.in is 21/05/2026 by 11:59 PM. The interview date will be conveyed after shortlisting.

Tel: 01332-28-4943

Email: abhaysah@ece.iitr.ac.in

  
**Dr. Abhay Kumar Sah**  
 Associate Professor  
 Electronics & Communication Engg.  
 Indian Institute of Technology Roorkee

\*To be uploaded on IIT Roorkee website and copy may be sent to appropriate address for wider circulation.

**APPROVED**

  
 Dean  
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