

INDIAN INSTITUTE OF TECHNOLOGY, ROORKEE
Electronics & Communication Engineering Department

Dated: ~~13~~-04-26

ADVERTISEMENT TO FILL UP PROJECT POSITIONS*

Applications are invited from Indian nationals only for project position(s) as per the details given below for the consultancy/research project(s) under the Principal investigator (Name: **Prof. Anand Bulusu.**), Dept./Centre **ECE Dept** Indian Institute of Technology, Roorkee.

1. Title of project: **Design and Development of ROM based low power AI inference chip**
2. Sponsor of the project: Divyasampark iHUB Roorkee for Devices Materials and Technology
3. Project position (SRF) 01
4. Qualifications: B.E/B.Tech in ECE or equivalent with 5-year experience with **GATE mandatory**
5. Emoluments: Rs. 42000 /- + HRA (as per institute rules)
6. Duration: Six Month
 - Extendible till the end of the project based on periodic review after six months.
7. Job description: The candidate should have a good knowledge of semiconductor device physics, network theory and VLSI circuit design. He/She should have an exposure to CMOS design logic.
 - EDA/Tools: Cadence tools, Spice, Layout-Editors, TCAD device/process simulation, TCAD mixed-mode simulation.
 - Hardware Descriptive language: Verilog or Verilog-A (desirable)
 - Scripting Language: Python, Perl or Bash
8. The candidate should have enthusiasm to coordinate and work at pace with collaborating industry.
9. Candidates before appearing for the interview shall ensure that they are eligible for the position they intend to apply.
10. Candidates desiring to appear for the Interview should submit their applications with the following documents to the office of Principal Investigator through email.
 - Application in a plain paper with detailed CV including chronological discipline of degree/certificates obtained.
 - Experience including research, industrial field and others.
 - Attested copies of degree/certificate and experience certificate.
11. Candidate shall bring along with them the original degree(s)/certificate(s) and experience certificate(s) at the time of interview for verification.
12. Preference will be given to SC/ST candidates on equal qualifications and experience.
13. Please note that no TA/DA is admissible for attending the interview.

Offline wall -in interview will be held at the Department of ECE IIT Roorkee (HOD Conference Room on April 24, 2026) at 2:30 PM. (to be given only for walk-in interview)

P. Anand

Tel: +91-1332-285347

**Name and signature
of Principal Investigator**

Email: vlsi-office@ece.iitr.ac.in

*To be uploaded on IIT Roorkee website and copy may be sent to appropriate addresses by PI for wider circulation.

APPROVED

[Signature]
 Dean

Sponsored Research & Industrial Consultancy
 Indian Institute of Technology Roorkee
 Roorkee-247 667 (INDIA)

[Signature] 14/04/2026 *[Signature]* 15/4/26 *[Signature]* 15/4/26