

INDIAN INSTITUTE OF TECHNOLOGY, ROORKEE
ADVERTISEMENT TO FILL UP PROJECT POSITIONS*

Dated: 27/10/2026

Applications are invited from Indian nationals only for project position(s) as per the details given below for the **research** project(s) under the Principal investigator **Prof. Karun Rawat**, Deptt./Centre of **Electronics & Communication Engineering**, Indian Institute of Technology, Roorkee.

1. Title of project : Design and implementation of Advance Techniques in Nonlinear Power Amplifiers- Optimizing Size, Weight and Power (SWAP)
2. Sponsor of the project : DRDO, New Delhi
3. Project position(s) and number : Project Scientist (1)
4. Qualifications (Eligibility) : Ph.D. degree or M.Tech./M.E. degree in Electronics/Electrical Engineering or a relevant discipline from a recognized university or institute.

Desirable Qualifications : At least one research publication in a Science Citation Indexed (SCI) journal or a reputed international conference. Proven research experience in RF circuit design, particularly in areas related to power amplifiers or RF/microwave circuits.
5. Emoluments : Rs. 56000 + HRA
6. Duration : 3 Years
7. Job description : The selected candidate will be involved in research and development activities related to the design and implementation of advanced techniques in non-linear RF power amplifiers with a focus on optimizing Size, Weight, and Power (SWaP). The candidate will work on analysis, design, simulation, fabrication, and testing of RF/microwave power amplifier circuits. The candidate is expected to carry out experimental validation, performance characterization, and integration of developed modules. He/She will be required to document research outcomes and publish findings in reputed journals and conferences. Additionally, the candidate will assist in preparing technical reports, project progress updates, and documentation for the sponsoring agency. He/She must adhere to the rules and regulations of the host institute/organization.

8. Terms & Conditions:

1. Candidates before appearing for the interview shall ensure that they are eligible for the position they intend to apply.
 2. Candidates desiring to appear for the Interview should submit their applications with the following documents through email by 08/11/2026:
 - CV
 - Degree Certificates
 - Experience Certificates
 3. Candidate shall bring along with them the original degree(s)/certificate(s) and experience certificate(s) at the time of interview for verification.
 4. Preference will be given to SC/ST candidates on equal qualifications and experience.
 5. Please note that no TA/DA is admissible for attending the interview.
9. Duration: 1 year or until the completion of the project.

The last date for application to be submitted to office of Principal Investigator is **08/11/2026** by **11:59 PM**.

Please submit your application via email to: **shankhraj.ece@srlic.iitr.ac.in**

Tel: 1332284830

Prof. Karun Rawat
Principal Investigator
Electronics & Communication Engineering
karun.rawat@ece.iitr.ac.in

Signature of Dean SRIC 
Dean
Sponsored Research & Industrial Consultancy
Indian Institute of Technology Roorkee
Roorkee-247 667 (INDIA)



*To be uploaded on IIT Roorkee website and copy may be sent to appropriate addresses by PI for wider circulation.