SRIC/06

INDIAN INSTITUTE OF TECHNOLOGY, ROORKEE
DEPARTMENT OF ELECTRICAL ENGINEERING

Dated: 09/03/2026

ADVERTISEMENT TO FILL UP PROJECT POSITIONS*

Applications are invited from Indian nationals only for project position(s) as per the details given below for the research project under the Principal investigators: **Dr. Apurv Kumar Yadav, Department of Electrical Engineering, Indian Institute of Technology Roorkee.**

1. Title of project: **The Smart Energy Metering and Billing System (SEMB): Enhancing EV Charging Infrastructure for Efficiency and Accessibility**
2. Sponsor of the project: **IIT Bhilai Innovation and Technology Foundation (IBITF, Bhilai)**
3. Project position(s) and number: **Junior Research Fellow (JRF) and 1 number**
4. Qualifications: Candidates that fulfil all the following qualification criteria are encouraged to apply for the position:
   A. Minimum of 4-year Bachelor's degree OR a Master's degree (M.Tech/ M.E./M.S. by research)
   B. Qualifying Degree Disciplines: Electrical or Electrical and Electronics Engineering with specialization in power electronics or Equivalent.
   C. Qualified in a national level test, such as- GATE / CEED / JEST / UGC-NET /CSIR-NET including lectureship (Assistant Professorship) / Ph.D. only. (Requirements of the national level tests such as GATE/NET is exempted for candidates having ≥ 8.0 CGPA in qualifying degree from the Ministry of Education funded Technical Institutions (erstwhile CFTIs of MoE))
5. Emoluments: **JRF: Rs. 37,000/- p.m. (for first two years)**
6. Duration: **2 years (The selected JRF candidate will have the opportunity to register for the Ph.D. program at IIT Roorkee as per institute norms)**
7. Job description:
   Power Electronics, GaN based AC-DC converter design, GaN based DC-DC converter
8. Terms:

- Candidates before appearing for the interview shall ensure that they are eligible for the position they intend to apply. Qualifications for the position are mentioned in point 4.
- Preference will be given to OBC/SC/ST candidates on equal qualifications and experience.
- Candidates should also make themselves familiar with job description (Point 7) beforehand.
- Candidates having experience in **hardware development of GaN based power converters** are encouraged to apply.
- Candidates desiring to appear for the Interview should submit their applications with the following documents to the office of principal investigator through email as **a Single PDF (mention "JRF_IBITF_<name>" in subject) in the following order:**
  - Detailed CV including chronological discipline of degree/certificates, papers, research experience, etc.
  - Copies of degree/certificate and experience certificate
  - Note: The selected candidate will get an opportunity to pursue a Ph.D. (Once a project is over candidate will be given MHRD fellowship as per the Institute policy)
- The last date for application along with necessary documents to be submitted by email to Principal Investigator (apurv@ee.iitr.ac.in) is 24th Mar 2026 by 5pm.
- The interview will be held online. Date, time and meeting link of interview will be communicated to the shortlisted candidates via email.
- The selected candidate can immediately join the project position.

APPROVED

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Sponsored Research & Industrial Consultancy
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