

INDIAN INSTITUTE OF TECHNOLOGY, ROORKEE
ADVERTISEMENT TO FILL UP PROJECT POSITIONS*

Dated: 13/8/2026

Applications are invited from Indian nationals only for project position(s) as per the details given below for the **research** project(s) under the Principal investigator **Prof. Parikshit Pareek**, Deptt./Centre of **Electrical Engineering**, Indian Institute of Technology, Roorkee.

1. Title of project : Establishing Avathon Physical AI Labs (Avathon Labs)
2. Sponsor of the project : Avathon Inc.
3. Project position(s) and number : Research Fellow (2)
4. Qualifications (Eligibility) :
 1. A four-year Bachelor's degree or a two-year Master's degree together with qualification in a national-level test such as GATE.
 2. A Bachelor's or Master's degree from an IIT with a CGPA of 8.0 or above. The national-level test requirement is waived.
- Desirable Qualifications : Machine Learning proficiency required.
Deep Learning and GenAI is preferred.
Mathematical Optimization is preferred.
5. Emoluments : Rs. 40000 - Rs. 60000 + HRA
6. Duration : 2 Years
7. Job description :

Work with Avathon AI Center (Bangalore and US)

Mathematical optimization and analysis. Build models of supply-chain problems, study their structure, and establish the properties - correctness, convergence, complexity, error bounds, that make a method worth trusting.

Algorithm design and implementation. Turn mathematics into algorithms, and algorithms into working code that runs at a scale where the difficulty is real rather than illustrative.

Research software and reproducibility. Maintain code that another researcher can read, run and extend: version-controlled, documented, tested, and released publicly wherever the project permits.

Experimental evaluation. Design benchmarks and experiments, choose honest baselines, and report what the evidence supports rather than what one had hoped to find.

Scientific communication. Write papers for peer-reviewed venues, present at group meetings and at periodic project reviews, and contribute to technical reports.

Collaboration and mentoring. Work alongside scholars reg-

istered in the other participating departments, and help guide junior students and project interns.

8. The selected candidate may get an opportunity for PhD admission.

9. Terms & Conditions:

1. Candidates before appearing for the interview shall ensure that they are eligible for the position they intend to apply.
2. Candidates desiring to appear for the Interview should submit their applications with the following documents through link by 31/8/2026:
 - CV
 - Degree Certificates
3. Candidate shall bring along with them the original degree(s)/certificate(s) and experience certificate(s) at the time of interview for verification.
4. Preference will be given to SC/ST candidates on equal qualifications and experience.
5. Please note that no TA/DA is admissible for attending the interview.

The last date for application to be submitted to office of Principal Investigator is **31/8/2026** by **11:59 PM**.

If selected, the candidate will be joining the position **as soon as possible**.

Please submit your application via the following link: <https://forms.gle/tPJGbidKh9bwDK4SA>

Tel:



Signature of Dean SRIC

Prof. Parikshit Pareek
Principal Investigator
Electrical Engineering

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*To be uploaded on IIT Roorkee website and copy may be sent to appropriate addresses by PI for wider circulation.

