

**INDIAN INSTITUTE OF TECHNOLOGY, ROORKEE
DEPARTMENT OF ELECTRICAL ENGINEERING**

Dated: 25/09/2026

ADVERTISEMENT TO FILL UP PROJECT POSITIONS*

Applications are invited from Indian nationals only for project position(s) as per the details given below for the research project under the Principal investigators: **Dr. Siba Kumar Patro, Department of Electrical Engineering, Indian Institute of Technology Roorkee.**

1. Title of project: **Enhanced Protection-enabled GaN based High Voltage Electronic Power Conditioner for traveling Wave Tube Amplifiers for Aerospace Applications**
2. Sponsor of the project: **Indian Space Research Organisation (ISRO, Bengaluru)**
3. Project position(s) and number:

Junior Research Fellow (JRF) and 1 number

4. Qualifications: **Candidates that fulfil following qualification criteria are encouraged to apply for the position:**

A. Minimum of 4-year Bachelor's degree OR a Master's degree

B. Qualifying Degree Disciplines: Electrical or Electrical and Electronics Engineering or Equivalent disciplines.

C. Qualified in a national level test, such as- GATE / CEED / JEST / UGC-NET /CSIR-NET including lectureship (Assistant Professorship) / Ph.D. only. (Requirements of the national level tests such as GATE/NET is exempted for candidates having ≥ 8.0 CGPA in qualifying degree from the Ministry of Education funded Technical Institutions (erstwhile CFTIs of MoE))

5. Emoluments:

JRF: Rs. 37,000/- p.m. (for first two years) SRF: Rs. 42,000/- p.m. (for third year)

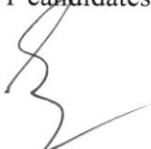
6. Duration: **3 years (The selected JRF candidate will have the opportunity to register for the Ph.D. program at IIT Roorkee as per institute norms)**

7. Job description:

Design and Development of Space grade GaN based DC-DC and AC-DC converters for Electronic Power Conditioner.

- A. Terms:

- a. Candidates before appearing for the interview shall ensure that they are eligible for the position they intend to apply. Qualifications for the position are mentioned in point 4.
- b. Preference will be given to OBC/SC/ST candidates on equal qualifications and experience.



- c. Candidates should also make themselves familiar with job description (Point 7) beforehand.
- d. Candidates who have experience in **Power Electronics Converter Design** Area are highly encouraged to apply
- e. Candidates desiring to appear for the Interview should submit their applications through the online form for which link is given below. Candidates will require following documents for filling the online form: i) Detailed CV including chronological discipline of degree/certificates obtained, papers published, research experience, etc. ii) Copies of degree/certificate and experience certificate.

Form link and QR Code: <https://forms.gle/TbwxdX1Xq2Cj7aq79>



- f. The last date for application submission is 11th October 2026
- g. The interview will be held online. Date and time of interview along with meeting link will be communicated to the shortlisted candidates via email.
- h. The selected candidate can immediately join the project position.

APPROVED

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