

## Mr. Ravindra Saini

### Technical Officer

Institute Instrumentation Centre  
Indian Institute of Technology Roorkee  
Roorkee-247667, India  
Office Tel: +91-1332-286292  
Email id: ravis.mic2014@iitr.ernet.in  
[ravisaini.15@gmail.com](mailto:ravisaini.15@gmail.com)



### Professional & Academic Qualification:

- **B.Tech.** in **Electronics and Telecommunications Engineering** from College of Engineering Roorkee, Roorkee, UPTU in the year 2008.
- **M.Tech** in **Electronics Engineering** from Homi Bhabha National Institute, Mumbai.

### Working Area:

- VLSI FPGA/RTL Designing
- VHDL
- Analog Circuit Design
- Digital Circuit Design
- Microprocessor/Microcontroller

### Working Experience:

6 Year of experience in Embedded System ,VLSI Front End Design, Digital and Analog Circuit Design.

Previously worked at Bhabha Atomic Research Centre (B.A.R.C), Mumbai (A Premier Multi-Disciplinary Research Centre of India) as a Scientific Officer-D.

### Responsibilities:

- Operation and maintenance of Analytical Instruments such as TEM, XRF etc.
- Website maintenance for IIC

### Publications:

- Initial efforts towards development of a scanning electron microscope; Y.V. Chaudhari, , R. Saini, ,Suvadip Pal, K.P. Ambastha, , B.P. Dubey, V.M. Joshi; International Conference on Electron Nanoscopy -2011.

- Design of Electron Beam Focusing Lens for Scanning Electron Microscope; S.S. Pany, R. Saini, Suvadip Pal, K.P. Ambastha, Y.V. Chaudhari, B.P. Dubey, V.M. Joshi; Symposium on Advanced Measurement Techniques and Instrumentations-2011.