

**DEPARTMENT OF ELECTRICAL ENGINEERING  
INDIAN INSTITUTE OF TECHNOLOGY ROORKEE**

**Open Notice for Ph.D. Viva -Voce Examination**

<b>Name of the Student</b>	<b>Mr. Chetan Shrivastava</b>
<b>Enrollment No.</b>	18914026
<b>Thesis Topic</b>	<b>Novel and Efficient Protection Schemes for Low Voltage DC Microgrid</b>
<b>Date and Day</b>	16 <sup>th</sup> February 2024, Friday
<b>Time</b>	4:00 PM to 5:30 PM
<b>Venue</b>	<b>Committee Room, Electrical Engineering Department +online for External Examiner (Webex) Meeting Link: <a href="https://iitroorkee.webex.com/iitroorkee/j.php?MTID=mc74b9a18dabb30fb356e0105f3da8e82">https://iitroorkee.webex.com/iitroorkee/j.php?MTID=mc74b9a18dabb30fb356e0105f3da8e82</a></b>
<b>Supervisor</b>	<b>Prof. Manoj Tripathy</b> <a href="https://ee.iitr.ac.in/~EE/manojfee">https://ee.iitr.ac.in/~EE/manojfee</a>
<b>External Examiner</b>	<b>Prof. Lie Xu</b> <b>Professor</b> , Department of Electronic & Electrical Engineering University of Strathclyde, 204 George Street, Glasgow G1 1XW, United Kingdom <a href="https://www.strath.ac.uk/staff/xuliedr/">https://www.strath.ac.uk/staff/xuliedr/</a>

All interested are invited.

Mukesh K Pathak

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<https://iitr.ac.in/Departments/Electrical%20Engineering%20Department/Home.html>